



PATENT  
455610-2580.1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Lawrence SALANT et al.  
Serial No.: 10/673,712  
Filed: September 29, 2003  
For: METHOD AND APPARATUS FOR BIT ERROR RATE ANALYSIS  
Examiner: Merant, Guerrier  
Art Unit: 2117  
Confirmation No.: 2458

745 Fifth Avenue  
New York, NY 10151

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on February 11, 2008.

William S. Frommer, Reg. No. 25,506

Name of Applicant, Assignee or Registered Representative

  
Signature

February 11, 2008

Date of Signature

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure set forth in 37 C.F.R. 1.56, applicants are filing this Information Disclosure Statement drawing the Examiner's attention to related applications Serial No. 10/673,736 filed September 29, 2003, Serial No. 10/673,735 filed September 29, 2003 and Serial No. 10/673,713 filed September 29, 2003, now Abandoned. The


Examiner's attention is directed to those documents identified in the enclosed form PTO-1449, copies of such documents are enclosed; if needed.

The filing of this Information Disclosure Statement is not an admission that the documents identified herein constitute prior art to the present application.

Please charge any fees incurred by the filing of this document or credit any overpayment to Deposit Account No. 50-0320.

Respectfully submitted,

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